

Notice of Allowability	Application No.	Applicant(s)	
	09/939,627	JEON, KI YONG	
	Examiner	Art Unit	
	Nguyen Ngo	2663	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to special amendment filed 12/19/2005.
2. ☒ The allowed claim(s) is/are 1-7,9-13,15 and 16.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| <ol style="list-style-type: none"> 1. <input type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____ |
|---|--|

DETAILED ACTION

Response to Amendment

1. This communication is in response to the special amendment filed on 12/19/05. Accordingly, Claims 1-7, 9-13, 15, and 16 are currently pending in the application.

Allowable Subject Matter

2. Claims 1-7, 9-13, 15, and 16 allowed.

The following is a statement of reasons for the indication of allowable subject matter:

3. Claim 1 is allowable over the prior art of record since the cited references taken individually or in combination fails to particularly disclose a VC mapping unit that maps a DS asynchronous signal to a VC signal as a byte unit, according to the mapping address and wherein the VC mapping unit comprises a plurality of elastic buffers that write the DS asynchronous signal as a bit unit and read the DS asynchronous signal as the byte unit comprising a parallel asynchronous signal of 8 bits. It is noted that the closest prior art, Russell et al. (US 6584118) discloses the method of mapping Ethernet data frames into SDH virtual container. However, Russell fails to disclose or render obvious to the above underline limitations as claimed.

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4. Claim 6 is allowable over the prior art of record since the cited references taken individually or in combination fails to particularly disclose an STM-1 address generating unit that generates a mapping address; a virtual container multiplexer, within the virtual container mapping unit, that multiplexes a number of the byte units received from the plurality of elastic buffers into the virtual containers; and a virtual container framer that identifies a mapping position of the asynchronous signal, based on the mapping address generated by the STM-1 address generating unit, and controls the multiplexing of the number of byte units into the virtual container. It is noted that the closest prior art, Russell et al. (US 6584118) discloses the method of mapping Ethernet data frames into SDH virtual container. However, Russell fails to disclose or render obvious to the above underlined limitations as claimed.

5. Claim 9 is allowable over the prior art of record since the cited references taken individually or in combination fails to particularly disclose that the read pointer generating unit increments or decrements the read address, after each of a number of the parallel data bits is read from the elastic buffer, by a value of four, six, or eight, and the value by which the read pointer generating unit is incremented or decremented depends on a number of null bits received in the serial asynchronous data. It is noted that the closest prior art, Russell et al. (US 6584118) discloses the method of

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mapping Ethernet data frames into SDH virtual container. However, Russell fails to disclose or render obvious to the above underline limitations as claimed.

6. Claim 13 is allowable over the prior art of record since the cited references taken individually or in combination fails to particularly disclose reading the parallel data units from the plurality of elastic buffers, according to generate read address; and writing the plurality of serial asynchronous data signal to the plurality of elastic buffers, according to generated write addresses, wherein a system clock controls the timing of writing the serial asynchronous data to the plurality of elastic buffers and reading the parallel data unit from the plurality of elastic buffers. It is noted that the closest prior art, Russell et al. (US 6584118) discloses the method of mapping Ethernet data frames into SDH virtual container. However, Russell fails to disclose or render obvious to the above underline limitations as claimed.

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nguyen Ngo whose telephone number is (571) 272-8398. The examiner can normally be reached on Monday-Friday 7am - 3:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ricky Ngo can be reached on (571) 272-3139. The fax

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phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Nguyen Ngo

United States Patent & Trademark Office
Patent Examiner AU 2663
(571) 272-8398


RICKY Q. NGO
SUPERVISORY PATENT EXAMINER